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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Chin-Chi TENG et al

Patent No: 6,751,786 B2

Issued: June 15, 2004

For: CLOCK TREE SYNTHESIS FOR A HIERARCHICALLY  
PARTITIONED IC LAYOUT

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT  
FOR PTO MISTAKE (37 CFR 1.322(a))

COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Attention: Patent Issue Division  
Decision and Certificate of Correction Branch

Certificate  
JUN 24 2004  
of Correction

Sir:

It is requested that a Certificate of Correction be issued in accordance with the form PTO-1050 that is enclosed herewith in duplicate.

The column and line numbers where the errors occur in the issued patent are specified below. The source of the correction is indicated in parentheses following the correction.

Column 10, line 38, "nods" should be deleted and replaced with --node--.  
(Claim 5 as originally filed).

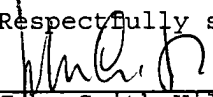
Column 12, line 23, "delay." should be deleted and replaced with  
--delays--. (Claim 13 as originally filed).

This patent is assigned of record to CADENCE DESIGN SYSTEMS, INC., the assignment having been recorded on July 8, 2003 at Reel 013774, Frame 0793.

This request is made by the undersigned as attorney for the patentee/assignee under the Power of Attorney that was signed on January 4, 2002 and is of record in the Patent and Trademark Office.

Please send the certificate to the address indicated below.

Respectfully submitted,

  
John Smith-Hill  
Reg. No. 27,730

SMITH-HILL & BEDELL, P.C.  
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Docket: SILI 2282  
Postcard: 06/04-26

Certificate of Mailing

I hereby certify that this correspondence is being deposited with sufficient postage as first class mail with the United States Postal Service in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on this June 24 day of 2004.

JUN 24 2004

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

PATENT NO. : 6,751,786 *B2*  
DATED : June 15, 2004  
INVENTOR(S) : Chin-Chi Teng and Wei-Jin Dai

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 10, line 38, "nods" should be deleted and replaced with --node--.

Column 12, line 23, "delay." should be deleted and replaced with --delays--.

MAILING ADDRESS OF SENDER:

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Portland, Oregon 97229

PATENT NO. 6,751,786 *B2*  
ATTORNEY DOCKET SILI 2282

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